

5.4 Configuring the Sources of the Model

Next, we will route a signal to each source in the circuit model. For more information about the circuit model sources, please refer to the [Sources](#) section.

- In the Configuration Tree, expand **Circuit Model >> Sources**. Note that a VeriStand channel has been created for each Source component in the circuit model.
- Click the **Sources** section to open the configuration page.
- Confirm that the source mappings are configured as shown in the image below.
- Click **Save**.

	Type	Group	Element
U01 Va	Voltage source	Sinewaves	Circuit Model 1.SWG00
U02 Vb	Voltage source	Sinewaves	Circuit Model 1.SWG01
U03 Vc	Voltage source	Sinewaves	Circuit Model 1.SWG02
U04 IaSCIM	Current source	Machines	SCIM 1.Stator Current Phase A (A)
U05 IbSCIM	Current source	Machines	SCIM 1.Stator Current Phase B (A)

The outputs of three **Sinewave Generators (SWG00, SWG01, SWG02)** are mapped to the AC Sources of the circuit model (*U01, U02, U03*), while the **Current Phase A** and **Current Phase B** outputs of the SCIM model are mapped to Sources *U04* and *U05*.

