

2.3 Updating the Circuit Model

The circuit model file has been loaded into the example project already, although the steps below can be used to reload the circuit model. This is typically only necessary if the circuit has been modified. For more information, please refer to [How to Add a Circuit Model to the System Definition](#).

- In the Configuration Tree, under **Power Electronics Add-On**, click **Circuit Model**.
- In the configuration page, click to **Browse** for the **Circuit Model File Path**.
- Select the model (.mdl) file at the **Circuit Model File Path** shown below. This automatically triggers a model update in the System Definition.
- Wait for the update to complete.
- (optional) Click the **Refresh** button to see additional model information.
- Click **Save**.

Circuit Model File Path: <Public Documents>National Instruments\<NI VeriStand 20XX>\Examples\OPAL-RT\Power Electronics Add-On\Two Level Inverter\Circuit Model\SPS\Two_Level_Inverter_SPS_Ica.mdl

The screenshot shows the 'System Explorer - Two Level Inverter.nivssdf' window. The left-hand tree view is expanded to 'Two Level Inverter', with 'Power Electronics Add-On' selected. The main area displays the 'Power Electronics Add-On Main Page' configuration. The 'Version' field is set to '1.7.0'. The 'Configuration' dropdown is set to 'eHSx64_Dual_PMSM_VDQ_IO_7868R'. The 'Configuration Description' text area lists the following details:

- Design built for NI PXIe-7868R (18 AO, 6 AI, 48 DIO)
- 1x eHSx64
- 1x Dual PMSM VDQ Model with Encoders and Resolvers
- 2x Hall Effect Sensors
- Signal Generators (Sine, PWM, SPWM)
- Analog Output Mapping and Rescaling
- Analog Input Rescaling
- Waveform Acquisition

Below the description is a table with two columns: 'Target' and 'Bitfile'. The first row shows 'FPGA0' in the Target column and 'eHSx64_Dual_PMSM_VDQ_IO_7868R.lvbitx' in the Bitfile column.

At the bottom, the 'Target Credentials' section contains two input fields: 'Username' with the value 'admin' and an empty 'Password' field.