

3.5 Configuring the Switches of the Model

Next, we will route a signal to each switch in the circuit model. For more information about the circuit model switches, please refer to the [Switches](#) section.

- In the Configuration Tree, expand **Circuit Model >> Switches**. Note that a VeriStand channel has been created for each Switch component in the circuit model.
- Click the **Switches** section to open the configuration page.
- Confirm that the switch mappings are configured as shown in the image below.
- Click **Save**.

	Type	Group		Element		Polarity	Gs
SW01_06 INVERTER_0	IGBT//Diode	SPWMs	▼	Circuit Model 1.SPWM00	▼	High	0.5
SW01_06 INVERTER_0	IGBT//Diode	SPWMs	▼	Circuit Model 1.!SPWM00	▼	High	0.5
SW01_06 INVERTER_0	IGBT//Diode	SPWMs	▼	Circuit Model 1.SPWM01	▼	High	0.5
SW01_06 INVERTER_0	IGBT//Diode	SPWMs	▼	Circuit Model 1.!SPWM01	▼	High	0.5
SW01_06 INVERTER_0	IGBT//Diode	SPWMs	▼	Circuit Model 1.SPWM02	▼	High	0.5
SW01_06 INVERTER_0	IGBT//Diode	SPWMs	▼	Circuit Model 1.!SPWM02	▼	High	0.5
SW07 Diode	Diode	CPU (VeriStand)	▼	SW07 Diode	▼	High	0.5
SW08 Diode	Diode	CPU (VeriStand)	▼	SW08 Diode	▼	High	0.5
SW09 Diode	Diode	CPU (VeriStand)	▼	SW09 Diode	▼	High	0.5
SW10 Diode	Diode	CPU (VeriStand)	▼	SW10 Diode	▼	High	0.5
SW11 Diode	Diode	CPU (VeriStand)	▼	SW11 Diode	▼	High	0.5
SW12 Diode	Diode	CPU (VeriStand)	▼	SW12 Diode	▼	High	0.5

The outputs of three **SPWM Generators** (**SPWM00**, **SPWM01**, **SPWM02**) and their complementary signals are mapped to the six inverter Switches of the circuit model (**SW01-06**). Switches **SW07** through **SW12** are diodes, so their mapping configuration settings will not affect the simulation.

