

PWM Generators Section

Use the PWM Generators to generate pulse width modulated signals on the FPGA. These signals can be mapped directly to **Digital Output** channels or circuit model **Switches**.

PWM Generators Section Channels

In the **System Explorer** window configuration tree, expand the **Power Electronics Add-On** custom device, then **Circuit Model >> PWM Generators** to display the following subsections and custom device channels. Assign values to the channels to configure the PWM Generators.

PWM Generator X					
	Channel Name	Type	Units	Default Value	Description
	Active Dead Time	Input	s	0s	Time during which both the PWM signal and its complementary are held in an inactive state, immediately before the PWM signal goes active.
	Duty Cycle	Input	%	0%	Fraction of one period for which the PWM signal is active, as a percentage
	Frequency	Input	Hz	1000Hz	Frequency of the generated PWM signal.
	Inactive Dead Time	Input	s	0s	Time during which both the PWM signal and its complementary are held in an inactive state, immediately before the complementary signal goes active.
PWM Reset					
	Channel Name	Type	Units	Default Value	Description
	PWM Reset	Input		0 (False)	<ul style="list-style-type: none">• False - Normal operation• True - Resets all PWMs