Migrating to 1.9.0

Migrating from 1.8.0 to 1.9.0

Project Dependency Update

When a VeriStand project created in a previous version of the Add-on is opened for the first time, a prompt is shown to the user stating that that the following files are missing on disk.

- *eHSx64_IM_IO_7868R.lvbitx*
- *hardware_configuration.json*

1. In the prompt, click on the **Skip** button for both dependencies and **Save** the project.
2. Open the S*ystem Definition* of the existing project. This will automatically trigger a dependency update.
3. **Save** and close the S*ystem Definition.*

PMSM BLDC JSON File Update

If using the **PMSM BLDC** machine model configured to use the **Variable Ld/Lq** motor type, the JSON file must be updated to include the **DqTransformAngleOffset** parameter.

1. Open the existing JSON parameter file in a text editor.
2. Add a new line under "*PolePairs":* and enter the following text: *"DqTransformAngleOffset": 0*
    a. For more information about this parameter, see the **JSON Motor Model File** description.
3. **Save** the JSON file.

Your project should now be compatible with version 1.9.0.

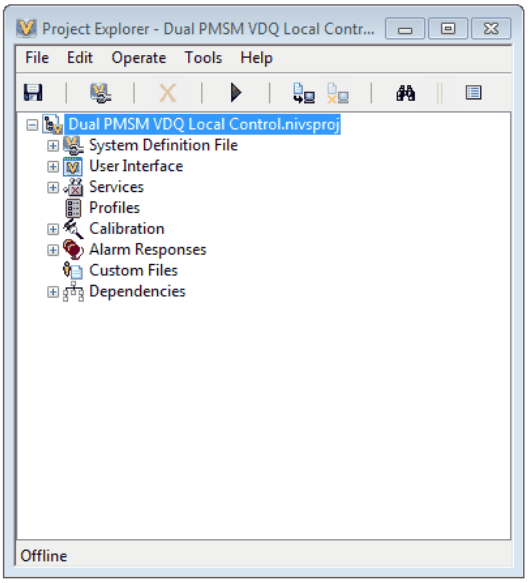
Migrating from 1.7.0 to 1.9.0

PMSM BLDC Machines

When using the following hardware configurations of the VeriStand Custom Device:

- *Dual_eHSx64_Quad_PMSM_VDQ_IO_Dual_7868R*
- *eHSx64_Dual_PMSM_VDQ_IO_7868R*

1. Open the S*ystem Definition* of the existing project.
2. Browse through the *System Definition Tree* and expand each **PMSM VDQ** section in the hardware configuration.
3. Rename each **PMSM VDQ** page to be **PMSM BLDC,** for consistency.
4. Make sure to complete the previous steps for each **PMSM BLDC** page in the hardware configuration.
5. If in your project you already mapped output channels from the **PMSM BLDC** into other resources such as *Digital Outputs*, *Analog Outputs* or *Wav eforms*, make sure to update that mapping to point to the **PMSM BLDC** instead of **PMSM VDQ**.
6. Save the S*ystem Definition.*

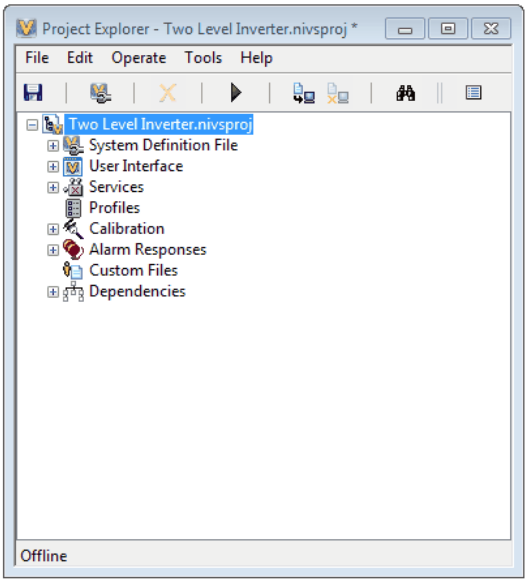


Hall Effect Pages

When using the following hardware configurations:

- *Dual_eHSx64_Quad_PMSM_VDQ_IO_Dual_7868R*
- *eHSx64_Dual_PMSM_VDQ_IO_7868R*
- *eHSx32_Dual_PMSM_SH_IO_7868R*

1. Open the S*ystem Definition* of the existing project.
2. Browse through the *System Definition Tree* and search for the **Hall Effect** pages under each machine.
3. Remove the all **Hall Effect** pages from the System Definition tree.
   Note: VeriStand will open three different dialog windows specifying that error 36 occurred (this will occur for each Hall Effect sensor removed). This is expected behavior due to removing the sensor. If these pages are not removed manually, the user will see an error upon deployment.
4. Save the S*ystem Definition.*



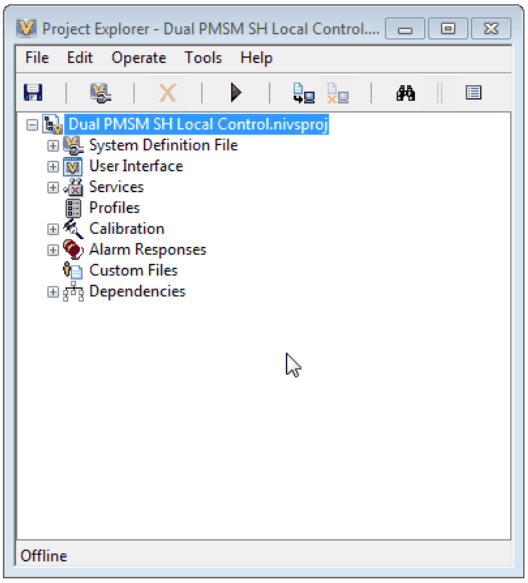
PMSM SH Machines

When using the following hardware configurations:

- *eHSx32_Dual_PMSM_SH_IO_7868R*
- *eHSx128_Dual_PMSM_SH_IO_7976R*

1. Open the S*ystem Definition* of the existing project.
2. Browse through the *System Definition Tree* and search for any of the two **PMSM SH** pages, (changing the sample time in one machine should change it for both).
3. Update the Sample time parameter to be 4E-7 (400ns).
4. Save the S*ystem Definition.*

> Your project should now be compatible with version 1.9.0.



Migrating from 1.6.X to 1.9.0

This procedure is required when upgrading a VeriStand project created in the 1.6.X version of the *Power Electronics Add-On.*

1. Open the S*ystem Definition* of the existing project. Automatically, an upgrade process of the system definition tree will be executed.
   Note: This process will generate a backup of the entire folder where the VeriStand project is saved. The backup will get stored in a zip file located in the project directory.
2. Browse through the *System Definition Tree* and click on the **Circuit Model** section of each circuit in the hardware configuration.
3. Click on the **Reload** button.
4. Follow the steps listed in Upgrading from 1.7.0 to 1.9.0
5. Save the S*ystem Definition.*

Your project should now be compatible with version 1.9.0.