

## 3.6 Setting Default Values for the Signal Generators

### Sinewave Generators

Next, we can configure the signal generators used in the simulation.

- In the Configuration Tree, expand **Circuit Model >> SPWM Generators**
- Configure the SPWM Generators by confirming that the **Default Values** of the relevant channels are configured as shown in the table below.
- Click **Save**.

Frequency Engines	
Channel Name	Default Value
Frequency Engine 0	60Hz
SWG 0	
Channel Name	Default Value
Frequency Engine	0
Phase	0°
SWG 1	
Channel Name	Default Value
Frequency Engine	0
Phase	120°
SWG 2	
Channel Name	Default Value
Frequency Engine	0
Phase	-120°

### SPWM Generators

Next, we can configure the **SPWM Generators** used in the simulation.

- In the Configuration Tree, expand **Circuit Model >> SPWM Generators**
- Configure the SPWM Generators by confirming that the **Default Values** of the relevant channels are configured as shown in the table below.
- Click **Save**.

SPWM Carrier	
Channel Name	Default Value
Carrier Frequency	5000Hz
SPWM Generator 0	
Channel Name	Default Value
Frequency Engine	0
Phase	0°
SPWM Generator 1	
Channel Name	Default Value
Frequency Engine	0

Phase	120°
<b>SPWM Generator 2</b>	
Channel Name	Default Value
Frequency Engine	0
Phase	-120°

System Explorer - Dual PMSM VDQ Local Control.nivssdf

File Edit Tools Help

**Model**

Name: Circuit Model 1

Description: Configure the circuit that will be simulated on the FPGA, including the Timestep and Scenarios.

Circuit Model File Path: C:\Users\Public\Docu...\Dual PMSM VDQ Local Control\Circuit Model\PMSM\_eHS\_UB.mdl Reload

Timestep (s): 2.45E-7      Form Factor: eHSx64

**Scenario Configuration**

Scenarios File Path: New

Use Scenarios?

**Model Information**

Minimum Timestep (s): 2.45E-7      Number of Scenarios Used: 0      Maximum Number of Scenarios: 38 Refresh