

2.4 Configuring the Sources of the Model

Next, we will route a signal to each source in the circuit model. For more information about the circuit model sources, please refer to the [Sources](#) section.

- In the Configuration Tree, expand **Circuit Model >> Sources**. Note that a VeriStand channel has been created for each Source component in the circuit model.
- Click the **Sources** section to open the configuration page.
- Confirm that the source mappings are configured as shown in the image below.
- Click **Save**.

	Type	Group	Element
U01	Voltage source	CPU (VeriStand)	U01

The **U01** Custom Device Channel, whose numeric value can be manipulated from the User Interface, is mapped to the circuit model Source (**U01**).

