

## 2.9 Controlling and Monitoring the Simulation

Once the VeriStand project has been successfully deployed, we can control and monitor the simulation. Open the **Workspace** file, *Two Level Inverter.nivscreen*, from the **Project Files** tab in the VeriStand Editor to view the user interface. Set **U01\_Vbat** to 220V and manipulate the parameters of the SPWM generators to view the results in the Waveform graphs.

