

Upgrading from 1.7.x to 1.8.0

These procedures are required when attempting to deploy a Veristand project created in the 1.7.x version of the *Power Electronics Add-On*, using the 1.8.x version of the *Power Electronics Add-On*.

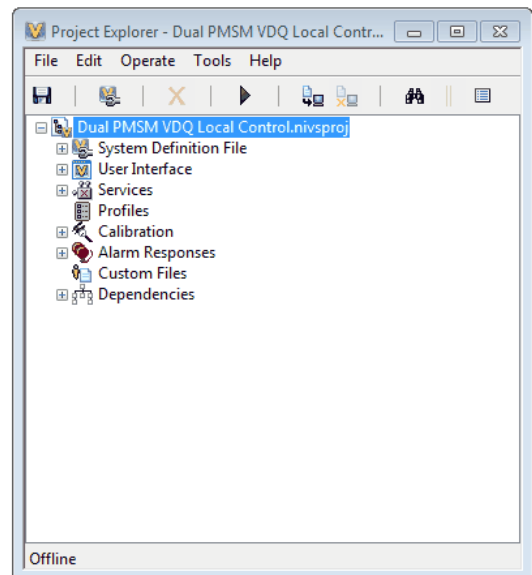
Depending on the hardware configuration you are using, you might need to follow one or more of the following procedures.

PMSM BLDC Machines

When using the following hardware configurations of the Veristand Custom Device:

- *Dual_eHSx64_Quad_PMSM_VDQ_IO_Dual_7868R*
- *eHSx64_Dual_PMSM_VDQ_IO_7868R*
- *eHSx128_Dual_PMSM_VDQ_IO_7976R*

1. Open the *System Definition* of the existing project.
2. Browse through the *System Definition Tree* and expand each **PMSM VDQ** section in the hardware configuration.
3. Click on the **PMSM VDQ** page. Notice how the **Back-EMF Phase A**, **Back-EMF Phase B** and the **Back-EMF Phase C** channels are included into the three after opening the **PMSM VDQ** page.
4. Rename each **PMSM VDQ** page to be **PMSM BLDC**, for consistency.
5. Make sure to complete the previous steps for each **PMSM BLDC** page in the hardware configuration.
6. If in your project you already mapped output channels from the **PMSM BLDC** into other resources such as *Digital Outputs*, *Analog Outputs* or *Waveforms*, make sure to update that mapping to point to the **PMSM BLDC** instead of **PMSM VDQ**.
7. Save the *System Definition*.

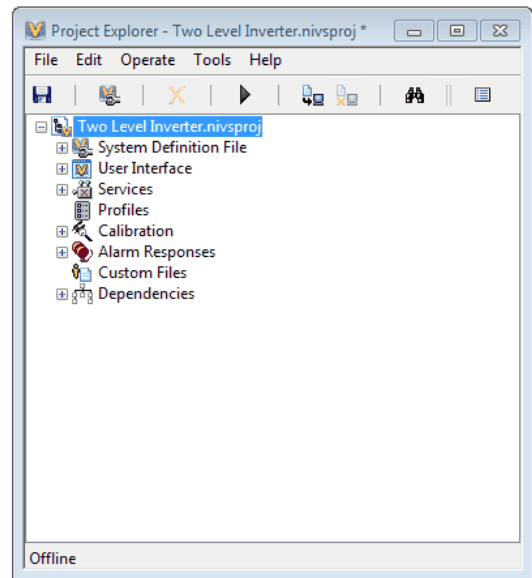


Hall Effect Pages

When using the following hardware configurations of the Veristand Custom Device:

- *Dual_eHSx64_Quad_PMSM_VDQ_IO_Dual_7868R*
- *eHSx64_Dual_PMSM_VDQ_IO_7868R*
- *eHSx32_Dual_PMSM_SH_IO_7868R*
- *eHSx128_Dual_PMSM_SH_IO_7976R*

1. Open the *System Definition* of the existing project.
2. Browse through the *System Definition Tree* and search for the **Hall Effect** pages, (each machine should have one).
3. Remove the all **Hall Effect** pages from the System Definition tree.
Note: Veristand will pop-up 3 times a window specifying that error 36 occurred per Hall Effect removed. This is an expected behavior of removing the sensor.
4. Save the *System Definition*.



PMSM SH Machines

When using the following hardware configurations of the Veristand Custom Device:

- *eHSx32_Dual_PMSM_SH_IO_7868R*
- *eHSx128_Dual_PMSM_SH_IO_7976R*

1. Open the *System Definition* of the existing project.
2. Browse through the *System Definition Tree* and search for any of the two **PMSM SH** pages, (changing the sample time in one machine should change it for both).
3. Update the Sample time parameter to be 4E-7 (400ns).
4. Save the *System Definition*.

