

OP5367 Specifications

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General

Model number Product description	OP5367-1 32 bits Digital Out	OP5367-3 16 bits Digital In, 16 bit Digital Out	OP5367-5 32 bits Digital In
Part Number	126-0616	126-0602	126-0604
Number of channels	D0-D31 Output	D0-D15 Input D16-D31 Output	D0-D31 Input
Board Type	Digital input/output conditioning		
Form Factor	Mezzanine OPAL-RT Type-B		

Characteristics

Digital Inputs	
Voltage Range	0-45V with programmable high/low threshold on the whole voltage range, per group of 8
Protection	±100VDC and ±100VAC
Input Impedance	> 100kOhms
Digital Outputs	
Type	Single-ended outputs
Voltage Range	3.3V or 5.0 V (LVTTTL, TTL)
Protection	Short Circuit Protection Protection limit: ±50VDC and ±50VAC, for single or multiple faults
Current limit	32mA/ch, 800 mA total
Output impedance	25 Ohms

Dynamic Characteristics

Maximum Frequency	>50 Mb/s
Latency	<20ns
FPGA Timing Resolution	20ns

Environmental



The OP5367 is designed for **indoor use only**.

Module operating temperature	0 °C to 40 °C
Storage temperature	-40 °C to 85 °C
Operating humidity	10% to 90% non-condensing
Storage humidity	5% to 95% non-condensing
Pollution Degree	2
Maximum altitude	2,000 m
Power requirement	12W