

# OP5351 - 32 Digital Input - TTL

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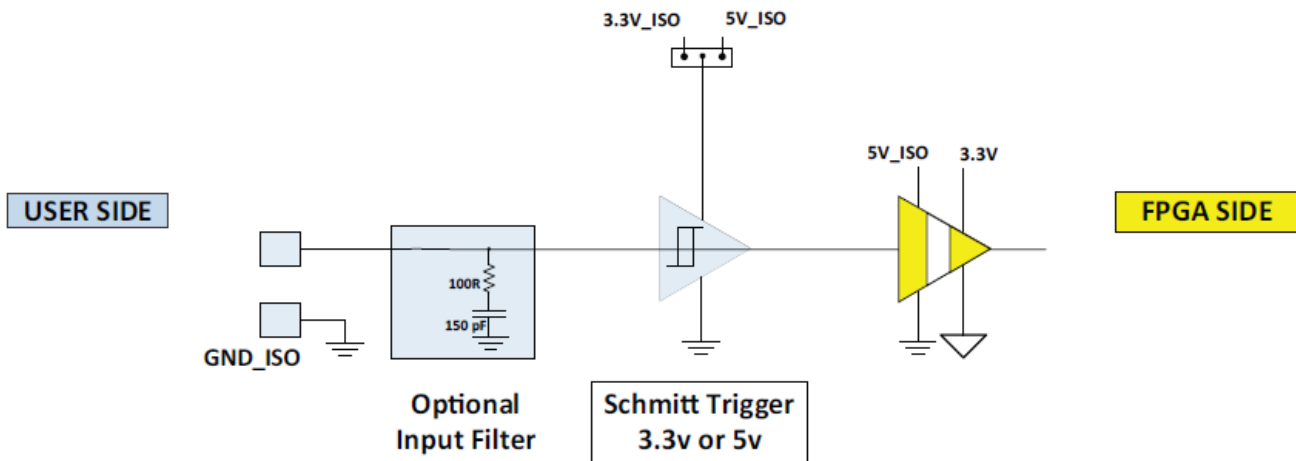
The OP5351 features 32 input channels operating at 5V (CMOS/TTL) or 3.3V (LVTTTL).

## Main Features

- The 32 channels are grouped into two banks of 16 channels each.
- The signals of each bank are made available to the user at one DB37F connector of the chassis in which the module is installed.
- The voltage input for each bank is configurable for either 5V (CMOS/TTL Logic ) or 3.3V (LVTTTL)
- The OP5351 is galvanically isolated to real environment signals, providing protection to the simulator from damaging voltages.
- All inputs are sampled simultaneously.

## Channel description

Each channel implements the circuit shown below:



## Voltage Jumper Settings

Each group of 16 channel can be configured for 5V or 3.3V operation, using the jumpers P3 and P4:

P3	CH00-15 (VIN_ISO1)	<ul style="list-style-type: none"> <li>• Place jumper over pins 1 and 2 to obtain +5V.</li> <li>• Place jumper over pins 2 and 3 to obtain 3V3</li> </ul>	
P4	CH16-31 (VIN_ISO2)	<ul style="list-style-type: none"> <li>• Place jumper over pins 1 and 2 to obtain +3V3.</li> <li>• Place jumper over pins 2 and 3 to obtain 5V</li> </ul>	

## Typical Use Cases

The card is ideal for interfacing with devices operating at low voltage.



TTL signals are not recommended for long-distance data transmission. We recommend using this board with cable lengths no longer than 1.5 meters (5 ft). When using longer lengths, OPAL-RT recommends terminating the lines to avoid over/undershoots or using alternative transmissions, such as differential mode (e.g: LVDS OP5355-3, RS422 OP5356-5 [5V] or OP5355-3 [3V3]).

For details on how to terminate the lines and to interconnect equipment with the OP5351 board, [refer to the WP CMOS/TTL opal RT\\_rev 1.0](#).

For compatibility of this card, please consult the [Software compatibility](#) and [Hardware compatibility](#) tables.